

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	105	(multi-bit adj2 symbol\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:40
L2	18	(ECC or (error adj2 correct\$4)) same (multi-bit adj2 symbol\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:47
L3	7	(ECC or (error adj2 correct\$4)) near4 encod\$4 same (multi-bit adj2 symbol\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:41
L4	7	(ECC or (error adj2 correct\$4)) near3 encod\$4 same (multi-bit adj2 symbol\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:41
L5	26	(ECC or (error adj2 correct\$4)) near3 encod\$4 and (multi-bit adj2 symbol\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:42
L6	50	(ECC or (error adj2 correct\$4)) near3 encod\$4 and ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:43
L7	11	(ECC or (error adj2 correct\$4)) near3 encod\$4 same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:43
L8	8	(ECC or (error adj2 correct\$4)) near3 encod\$4) near15 ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:43
L9	0	(ECC or (error adj2 correct\$4)) near3 encod\$4 same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) same controller	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:43
L10	5	(ECC or (error adj2 correct\$4)) near3 encod\$4 same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) same (buffer or storage\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:44
L11	0	(ECC or (error adj2 correct\$4)) near3 encod\$4 same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) same row\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:44
L12	1	(ECC or (error adj2 correct\$4)) near3 encod\$4 same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1)) same ((memory adj2 cell\$1) or row\$1 or (memory adj2 array))	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:45

L13	4	((ECC or (error adj2 correct\$4) near3 encod\$4) same ((multi-bit adj2 symbol\$1) or (n-bit adj2 symbol\$1))) and ((memory adj2 cell\$1) or row\$1 or (memory adj2 array))	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:46
L14	95	(ECC or (error adj2 correct\$4) adj2 encod\$4) same ((multi-bit adj2 symbol\$1) or n-bit\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:48
L16	10	(ECC or (error adj2 correct\$4) adj2 encod\$4) same ((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:49
L17	41	(ECC or (error adj2 correct\$4) adj2 encod\$4) and ((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 13:50
L18	15	(ECC or (error adj2 correct\$4) adj2 encod\$4) and ((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1)) and ((memory adj2 cells) or (memory adj2 array) or row\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:01
L19	0	(ECC or (error adj2 correct\$4) adj2 encod\$4) and (((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1)) near10 row\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:01
L20	0	(ECC or (error adj2 correct\$4) adj2 encod\$4) and (((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1)) same row\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:02
L21	2	(((multi-bit adj2 symbol\$1) or (n-bit\$1 adj2 symbol\$1)) same row\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:02
L22	681	(multi-bit same row\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:03
L23	284	(multi-bit near10 row\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:03
L24	162	(multi-bit near4 row\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:03
L25	15	(multi-bit near4 row\$1 near4 stor\$3)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:05
L26	2	(stor\$3 adj3 multi-bit\$1 adj3 row\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:06

L27	8	((stor\$3 adj3 multi-bit\$1) adj10 row\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:07
L28	0	((stor\$3 adj3 multi-bit\$1) adj10 row\$1) and ECC	USPAT; EPO; JPO; DERWENT	OR	OFF	2004/11/19 14:07



» See

Help FAQ Terms IEEE Peer Review

Quick Links

Welcome to IEEE Xplore

- Home
- What Can I Access?
- Log-out

TYPE OF CONTENTS

- Journals & Magazines
- Conference Proceedings
- Standards

SEARCH

- By Author
- Basic
- Advanced
- CrossRef

MEMBER SERVICES

- Join IEEE
- Establish IEEE Web Account
- Access the IEEE Member Digital Library

IEEE Xplore

- Access the IEEE Enterprise File Cabinet

 Print Format

Your search matched **263** of **1094442** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or enter a new one in the text box.

 Check to search within this result set**Results Key:****JNL** = Journal or Magazine **CNF** = Conference **STD** = Standard**1 A design of Reed-Solomon decoder with systolic-array structure***Iwamura, K.; Dohi, Y.; Imai, H.;*Computers, IEEE Transactions on , Volume: 44 , Issue: 1 , Jan. 1995
Pages:118 - 122[\[Abstract\]](#) [\[PDF Full-Text \(332 KB\)\]](#) **IEEE JNL****2 New array codes for multiple phased burst correction***Blaum, M.; Roth, R.M.;*Information Theory, IEEE Transactions on , Volume: 39 , Issue: 1 , Jan. 1993
Pages:66 - 77[\[Abstract\]](#) [\[PDF Full-Text \(936 KB\)\]](#) **IEEE JNL****3 Application of erasure-only decoded Reed-Solomon codes in cell recovery for congested ATM networks***Kamali, B.; Morris, P.;*Vehicular Technology Conference, 2000. IEEE VTS-Fall VTC 2000. 52nd , Volu
2 , 24-28 Sept. 2000
Pages:983 - 986 vol.2[\[Abstract\]](#) [\[PDF Full-Text \(348 KB\)\]](#) **IEEE CNF****4 Reed-Solomon codecs for optical communications***Popovici, E.M.; Fitzpatrick, P.;*Microelectronics, 2002. MIEL 2002. 23rd International Conference on , Volume
2 , 12-15 May 2002
Pages:613 - 616[\[Abstract\]](#) [\[PDF Full-Text \(452 KB\)\]](#) **IEEE CNF**

5 A Reed-Solomon decoder with the efficient recursive cell architecture for DVD application

Dong-Hoon Lee; Seung-Wook Lee; Jong Tae Kim;
Consumer Electronics, 2001. ICCE. International Conference on , 19-21 June 1
Pages:184 - 185

[\[Abstract\]](#) [\[PDF Full-Text \(200 KB\)\]](#) [IEEE CNF](#)

6 Fast parallel algorithms for decoding Reed-Solomon codes

Dabiri, D.; Blake, I.F.;
Information Theory, 1994. Proceedings., 1994 IEEE International Symposium on , 27 June-1 July 1994
Pages:97

[\[Abstract\]](#) [\[PDF Full-Text \(60 KB\)\]](#) [IEEE CNF](#)

7 VLSI array architecture for Reed-Solomon decoding

Arambepola, B.; Choomchuay, S.;
Circuits and Systems, 1991., IEEE International Symposium on , 11-14 June 1
Pages:2963 - 2966 vol.5

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) [IEEE CNF](#)

8 A fast multispeed comma-free Reed-Solomon decoder for W-CDMA applications using foldable systolic array architecture

Chi-Fang Li; Wern-Ho Sheen; Chong-Ren Wang; Yuan-Sun Chu;
Solid-State Circuits, IEEE Journal of , Volume: 38 , Issue: 4 , April 2003
Pages:677 - 682

[\[Abstract\]](#) [\[PDF Full-Text \(464 KB\)\]](#) [IEEE JNL](#)

9 A versatile time-domain Reed-Solomon decoder

Shayan, Y.R.; Le-Ngoc, T.; Bhargava, V.K.;
Selected Areas in Communications, IEEE Journal on , Volume: 8 , Issue: 8 , Oct 1990
Pages:1535 - 1542

[\[Abstract\]](#) [\[PDF Full-Text \(584 KB\)\]](#) [IEEE JNL](#)

10 An efficient recursive cell architecture of modified Euclid's algorithm for decoding Reed-Solomon codes

Sang Seol Lee; Moon Kyou Song;
Consumer Electronics, IEEE Transactions on , Volume: 48 , Issue: 4 , Nov 2000
Pages:845 - 849

[\[Abstract\]](#) [\[PDF Full-Text \(369 KB\)\]](#) [IEEE JNL](#)

11 Efficient encoding and minimum distance bounds of Reed-Solomon array codes

Mittelholzer, T.;
Information Theory, 2002. Proceedings. 2002 IEEE International Symposium on , 2002

Pages:282

[\[Abstract\]](#) [\[PDF Full-Text \(201 KB\)\]](#) [IEEE CNF](#)

12 On the VLSI design of a pipeline Reed-Solomon decoder using systolic arrays

Shao, H.M.; Reed, I.S.;

Computers, IEEE Transactions on, Volume: 37, Issue: 10, Oct. 1988

Pages:1273 - 1280

[\[Abstract\]](#) [\[PDF Full-Text \(520 KB\)\]](#) [IEEE JNL](#)

13 An area effective standard cell based channel decoder LSI for digital satellite TV broadcasting

Kamada, T.; Fukuoka, T.; Nakai, Y.; Nakakura, Y.; Ueda, K.; Ota, K.; Shiomi, Fukumoto, Y.;

VLSI Signal Processing, IX, 1996., [Workshop on], 30 Oct.-1 Nov. 1996

Pages:337 - 346

[\[Abstract\]](#) [\[PDF Full-Text \(500 KB\)\]](#) [IEEE CNF](#)

14 A comma-free Reed-Solomon decoder chip for W-CDMA/FDD applications

Chi-Fang Li; Chong-Ren Wang; Yuan-Sun Chu; Wern-Ho Sheen;

ASIC, 2002. Proceedings. 2002 IEEE Asia-Pacific Conference on, 6-8 Aug. 2002

Pages:355 - 358

[\[Abstract\]](#) [\[PDF Full-Text \(386 KB\)\]](#) [IEEE CNF](#)

15 Design and implementation of error detection and correction circuit for multilevel memory protection

Polianskikh, B.; Zilic, Z.;

Multiple-Valued Logic, 2002. ISMVL 2002. Proceedings 32nd IEEE International Symposium on, 15-18 May 2002

Pages:89 - 95

[\[Abstract\]](#) [\[PDF Full-Text \(315 KB\)\]](#) [IEEE CNF](#)

[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [11](#) [12](#) [13](#) [14](#) [15](#) [16](#) [17](#) [18](#) [Next](#)